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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James M. Derderian

Examiner: Alexander O Williams

Patent No.: 7,518,223

Group Art Unit: 2826

Issue Date: April 14, 2009

Docket No: 2001-0106.00/US

Title: SEMICONDUCTOR DEVICES AND SEMICONDUCTOR DEVICE ASSEMBLIES INCLUDING A
NONCONFLUENT SPACER LAYER

Commissioner for Patents
Office of Patent Publication
ATTN: Certificate of Correction Branch

Certificate
JUN 11 2009
of Correction

We are transmitting herewith the attached:

- ☒ Request for Certificate of Correction.
- ☒ Certificate of Correction Form - PTO-1050 (in duplicate)
- ☒ A return postcard.

Please charge any additional fees or credit overpayment to Deposit Account No. 13-3092.

Customer Number: 26809

Respectfully submitted,

//Richard Henkler//


Date: June 5, 2009

By: _____
Rich Henkler
Reg. No. 39,220

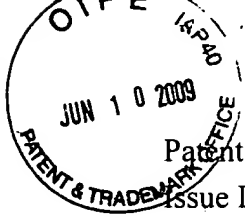
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria VA 22313-1450, on this 5th day of June 2009.

Jenny Mundt

Name



Signature



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Patentee: James M. Derderian

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REQUEST FOR CERTIFICATION OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Certificate of Correction Branch

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

As the error is that of the Patent Office, it is believed that no fee is due.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

Respectfully submitted,

Customer Number: 26809

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,518,223

Page (1) of 1

DATED : April 14, 2009

INVENTOR(S) : Derderian

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 14, line 36, in Claim 41, before "semiconductor" insert - - The - -.

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,518,223

Page (1) of 1

DATED : April 14, 2009

INVENTOR(S) : Derderian

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In column 14, line 36, in Claim 41, before "semiconductor" insert - - The - -.